



 Substitute for Form 1449 A & B/PTO		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number Confirmation Number Filing Date First Named Inventor Art Unit Examiner Name	10/765,931 1863 January 29, 2004 Youichi KUKIMOTO 2823 Khiem D. Nguyen
Sheet	1	of	1
		Attorney Docket Number	
		Q79041	

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
K.M.		Copy of Office Action from Japanese Patent Office dated May 2, 2006.	

08/03/06

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MODIFIED PTO/SB/08 A & B (08-03)

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		Confirmation Number	1863
		Filing Date	January 29, 2004
		First Named Inventor	Youichi KUKIMOTO
		Art Unit	2823
		Examiner Name	Khiem D. Nguyen
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K.N.		Seiki SAKUYAMA, "Solder Bumping Technology for Wafer-scale Packaging", 7 th Symposium on "Microjoining and Assembly Technology in Electronics", February 1-2, 2001, pp 285-290.	Abstract

Examiner Signature Date Considered 08/03/06

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